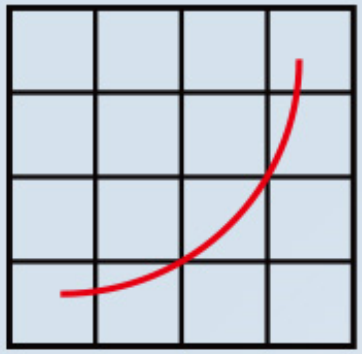

SPEC 2016亚洲峰会
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Symposium on Server Efficiency

Klaus-Dieter Lange

SPECpower Chair

Agenda - Thursday October 27

13:00	**Welcome to the SPEC Symposium'16 on Server Efficiency** Klaus-Dieter Lange (HPE), SPECpower Chair
13:20	**Designing a Good Benchmark – Part 2** Jeremy Arnold (IBM), SPECpower Architect
14:00	**Server Efficiency Rating Tool (SERT) Overview (Design, Metric)** SPECpower Committee
15:00	**Tea Break**
15:30	**ISO and Global Government Programs** Henry Wong (ISO)
16:00	**Trends in Server Efficiency and Power Usage in Data Centers** Sanjay Sharma (Intel), SPECpower Vice-Chair
16:30	**The Future of Server Efficiency and how it will be measured** Klaus-Dieter Lange (HPE/SPEC), Henry Wong (ISO), David Reiner (AMD/TGG), and Sanjay Sharma (Intel)
17:30	**Closing Remarks**

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Industry Challenge

Server Option	Platform A 18,831,605,760*
CPU	**44 combinations** 22 types, 1 or 2 populated
Memory	**144 combinations** 6 types, 1 to 24 populated
Internal Storage	**1440 combinations** 80 types, 1 to 18 populated
Additional Card	**172 combinations** 43 types. 4 slots
PSUs	**12 combinations** 6 types, 1 or 2 populated

*Realistic combinations

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SPECpower Committee

Working Group was established on January 26, 2006

Create SPEC products and assist other subcommittees, groups and organizations in creating products for fairly and consistently reporting system energy or power usage.

Group was granted committee status on 4/27/2006

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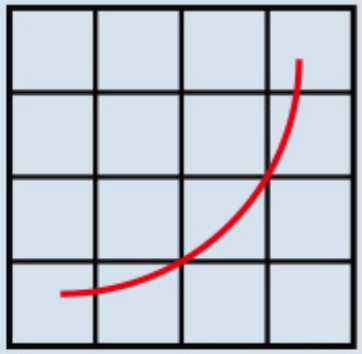

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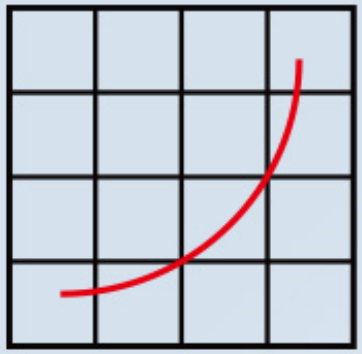
Q&A



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Thank you!

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